## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Divisional Application of 09/940,625:

Katsuhito Kikuma, Mitsutaka Ikeda, Yoshihiro Tsukidate, Yuji Akashi, Kaname Ozawa, Akira Takashima and Takao Nishimura

Group Art Unit: Not Yet Assigned

Serial No.: Filed Concurrently

Examiner: Not Yet Assigned

Filed: July 22, 2003

P.T.O. Confirmation No.:

FOR: STACKED SEMICONDUCTOR DEVICE AND METHOD OF PRODUCING THE SAME

## **INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 CFR 1.97(b)**

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

July 22, 2003

Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/940,625.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

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DWH/nk

Atty. Docket No. 010944A Suite 1000,1725 K Street, N.W. Washington, D.C. 20006 (202) 659-2930

Enclosure: PTO-1449

PATENT TRADEMARK OFFICE

INFORMATION DISCLOSURE			Atty. Docket No. 010944A			Serial No. Filed Concurrently		
CITATION PTO-1449		Applicant(s): Katsuhito Kikuma, Mitsutaka Ikeda, Yoshihiro Tsukidate, Yuji Akashi, Kaname Ozawa, Akira Takashima and Takao Nishimura						
			Filing Date: July 22, 2003			Group Art Unit: Not Yet Assigned		
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Date Considered

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